99
PLUS Search Results for S/N 09755499, Searched January 25, 2005

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ch
system for U.S. Patents from 1971 to the present. PLUS is a
query-by-example search system which produces a list of patents that a
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most closely related linguistically to the application searched. This
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Information Center, SIRA.

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5337312 56
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5841989 54
5457683 54

09755499_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 09755499 on January 25, 2005

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12   710/5          (4 OR,  8 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                          PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
        710/5             .Input/Output command process

 9   710/52         (6 OR,  3 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                          PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
        710/52            .Input/Output data buffering

 7   710/36         (0 OR,  7 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                          PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
        710/36            .Input/Output access regulation

 7   710/40         (0 OR,  7 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                          PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
        710/36            .Input/Output access regulation
        710/40            ..Access prioritization

 6   710/6          (3 OR,  3 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                          PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
        710/5             .Input/Output command process
        710/6             ..Operation scheduling

 5   710/20         (0 OR,  5 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                          PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
        710/20            .Concurrent Input/Output processing and data
                            transfer

 5   710/21         (0 OR,  5 XR)
        Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                          PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
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         710/20         .Concurrent Input/Output processing and data
                          transfer
         710/21         ..Concurrent data transferring


5   710/240         (2 OR, 3 XR)
         Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/240         ACCESS ARBITRATING


5   710/7           (0 OR, 5 XR)
         Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/1           INPUT/OUTPUT DATA PROCESSING
         710/5           .Input/Output command process
         710/7           ..Concurrently performing Input/Output
                          operation and other operation unrelated to
Input/Output


4   710/107         (0 OR, 4 XR)
         Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/100         INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                           TRANSACTION PROCESSING)
         710/107         .Bus access regulation


4   710/113         (3 OR, 1 XR)
         Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/100         INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                           TRANSACTION PROCESSING)
         710/107         .Bus access regulation
         710/113         ..Centralized bus arbitration


4   710/306         (2 OR, 2 XR)
         Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/100         INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                           TRANSACTION PROCESSING)
         710/305         .Bus interface architecture
         710/306         ..Bus bridge


4   710/33          (0 OR, 4 XR)
         Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/1           INPUT/OUTPUT DATA PROCESSING
         710/33          .Data transfer specifying


4   710/39          (0 OR, 4 XR)
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          Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:  INPUT/OUTPUT
          710/1            INPUT/OUTPUT DATA PROCESSING
          710/36           .Input/Output access regulation
          710/39           ..Access request queuing

    4  710/53          (0 OR,  4 XR)
          Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:  INPUT/OUTPUT
          710/1            INPUT/OUTPUT DATA PROCESSING
          710/52           .Input/Output data buffering
          710/53           ..Alternately filling or emptying buffers

    4  711/141         (0 OR,  4 XR)
          Class   711 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS: MEMORY
          711/100          STORAGE ACCESSING AND CONTROL
          711/117          .Hierarchical memories
          711/118          ..Caching
          711/141          ...Coherency

    3  709/201         (0 OR,  3 XR)
          Class   709 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS:  MULTIPLE COMPUTER OR PROCESS    COO
RDINATING
          709/200          MULTICOMPUTER DATA TRANSFERRING
          709/201          .Distributed data processing

    3  709/231         (2 OR,  1 XR)
          Class   709 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS:  MULTIPLE COMPUTER OR PROCESS    COO
RDINATING
          709/200          MULTICOMPUTER DATA TRANSFERRING
          709/230          .Computer-to-computer protocol implementing
          709/231          ..Computer-to-computer data streaming

    3  710/104         (2 OR,  1 XR)
          Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:  INPUT/OUTPUT
          710/100          INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
          710/104          .System configuring

    3  710/106         (1 OR,  2 XR)
          Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:  INPUT/OUTPUT
          710/100          INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
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        710/105        .Protocol
        710/106        ..Using transmitter and receiver

  3  710/112        (0 OR, 3 XR)
        Class    710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:   INPUT/OUTPUT
        710/100           INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
        710/107           .Bus access regulation
        710/112           ..Bus request queuing

  3  710/56         (0 OR, 3 XR)
        Class    710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:   INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
        710/52            .Input/Output data buffering
        710/56            ..Buffer space allocation or deallocation

  3  710/58         (0 OR, 3 XR)
        Class    710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:   INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
        710/58            .Input/Output process timing

  3  710/8          (0 OR, 3 XR)
        Class    710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA
                            PROCESSING SYSTEMS:   INPUT/OUTPUT
        710/1             INPUT/OUTPUT DATA PROCESSING
        710/8             .Peripheral configuration

  2  340/825.52     (0 OR, 2 XR)
        Class    340 :   COMMUNICATIONS:   ELECTRICAL
        340/825           SELECTIVE
        340/825.52        .Addressing

  2  709/212        (2 OR, 0 XR)
        Class    709 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS:   MULTIPLE COMPUTER OR PROCESS     COO
RDINATING
        709/200           MULTICOMPUTER DATA TRANSFERRING
        709/212           .Computer-to-computer direct memory accessing


  2  709/230        (0 OR, 2 XR)
        Class    709 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS:   MULTIPLE COMPUTER OR PROCESS     COO
RDINATING
        709/200           MULTICOMPUTER DATA TRANSFERRING
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          709/230        .Computer-to-computer protocol implementing

  2  709/232        (1 OR, 1 XR)
        Class   709 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                        SYSTEMS:   MULTIPLE COMPUTER OR PROCESS    COO
RDINATING
          709/200        MULTICOMPUTER DATA TRANSFERRING
          709/230        .Computer-to-computer protocol implementing
          709/232        ..Computer-to-computer data transfer regulatin
g


  2  710/244        (0 OR, 2 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:   INPUT/OUTPUT
          710/240        ACCESS ARBITRATING
          710/244        .Access prioritizing

  2  710/305        (0 OR, 2 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:   INPUT/OUTPUT
          710/100        INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
          710/305        .Bus interface architecture

  2  710/309        (0 OR, 2 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:   INPUT/OUTPUT
          710/100        INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
          710/305        .Bus interface architecture
          710/306        ..Bus bridge
          710/309        ...Arbitration

  2  710/310        (0 OR, 2 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:   INPUT/OUTPUT
          710/100        INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
          710/305        .Bus interface architecture
          710/306        ..Bus bridge
          710/310        ...Buffer or que control

  2  710/311        (0 OR, 2 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:   INPUT/OUTPUT
          710/100        INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
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          710/305        .Bus interface architecture
          710/306        ..Bus bridge
          710/311        ...Intelligent bridge

 2   710/41          (0 OR, 2 XR)
          Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
          710/1          INPUT/OUTPUT DATA PROCESSING
          710/36         .Input/Output access regulation
          710/40         ..Access prioritization
          710/41         ...Dynamic

 2   710/48          (1 OR, 1 XR)
          Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
          710/1          INPUT/OUTPUT DATA PROCESSING
          710/36         .Input/Output access regulation
          710/48         ..Input/Output interrupting

 2   710/62          (1 OR, 1 XR)
          Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
          710/1          INPUT/OUTPUT DATA PROCESSING
          710/62         .Peripheral adapting

 2   712/30          (0 OR, 2 XR)
          Class   712 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS:  PROCESSING ARCHITECTURES AND    INS
TRUCTION
                           PROCESSING
          712/1          PROCESSING ARCHITECTURE
          712/28         .Distributed processing system
          712/30         ..Operation

 2   712/8           (0 OR, 2 XR)
          Class   712 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS:  PROCESSING ARCHITECTURES AND    INS
TRUCTION
                           PROCESSING
          712/1          PROCESSING ARCHITECTURE
          712/2          .Vector processor
          712/7          ..Vector processor operation
          712/8          ...Sequential

 2   718/102         (0 OR, 2 XR)
          Class   718 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS: VIRTUAL MACHINE TASK OR PROCESS MAN
 AGEMENT OR TASK
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                    09755499_CLSTITLES
                    MANAGEMENT/CONTROL
718/100             TASK MANAGEMENT OR CONTROL
718/102             .Process scheduling
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